

AMENDMENTS TO THE DRAWINGS:

Please note the proposed corrections to Figs. 1-7, 11 and 25 as shown in red on the copies of the drawings attached herewith. Specifically, applicant proposes to amend the drawings as follows:

- (1) In FIGS. 1 – 7, add “PRIOR ART”,
- (2) In FIGS. 5 – 7, change “LABAL” or “LABLE” to --LABEL--,
- (3) In FIG. 11, interchange “PORT GROUP 3” and “PORT GROUP 4”, and
- (4) In FIG. 25, change “80 FORWARDER” to --80 FORWARDER (X)--.

Applicant will submit formal substitutes upon approval by the Examiner.



FIG.1
Prior Art

EXAMPLE OF ARCHITECTURE OF LSR AS ATM-SWITCH BASE MPLS
EDGE DEVICE (IP/MPLS FORWARDER IS MOUNTED IN CPU)

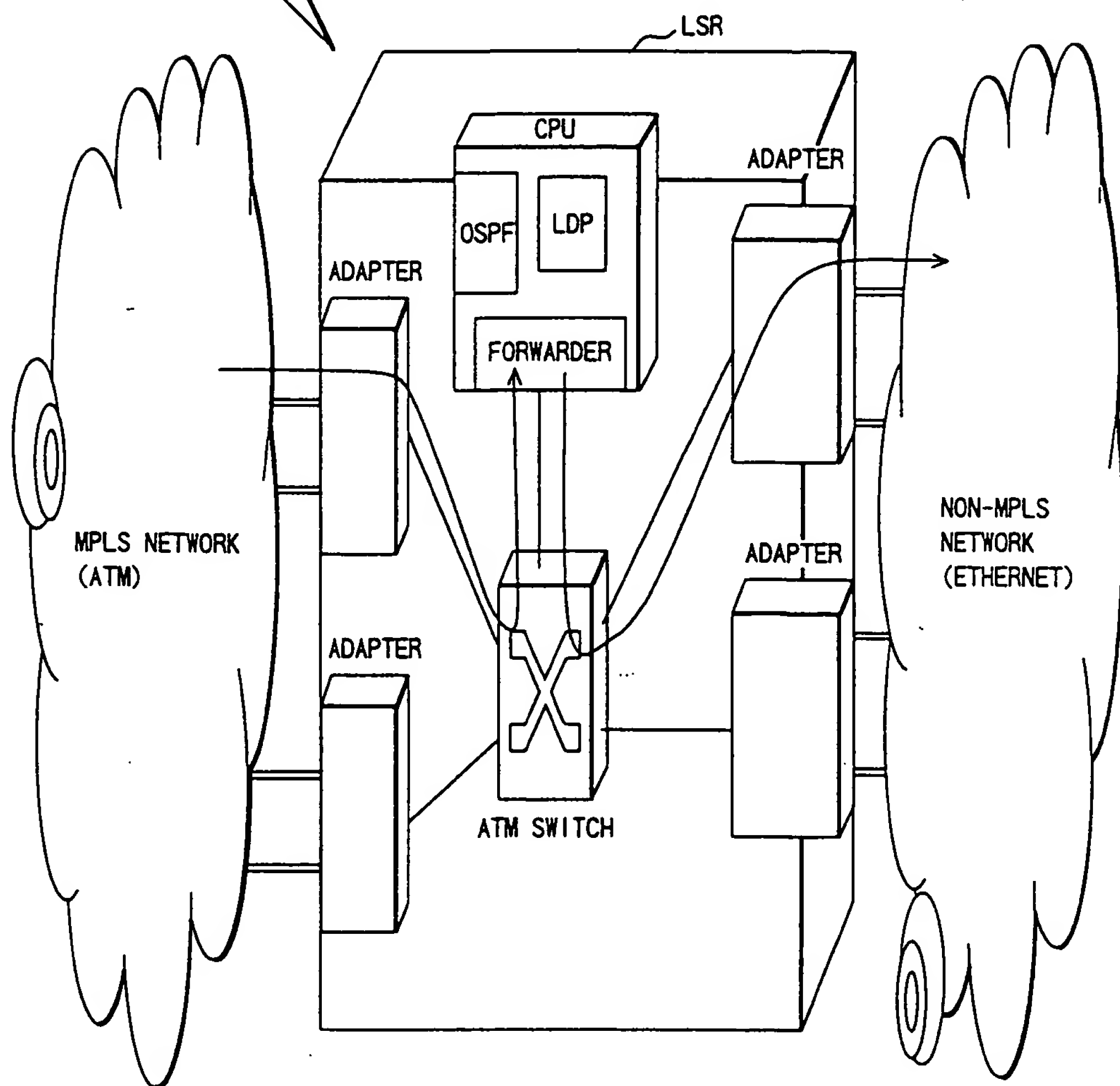


FIG.2
Prior Art

EXAMPLE OF ARCHITECTURE OF LSR AS ATM-SWITCH BASE MPLS
EDGE DEVICE (IP/MPLS FORWARDER IS MOUNTED AT PRE-STAGE
OF CPU AND SHORT-CUTS CPU)

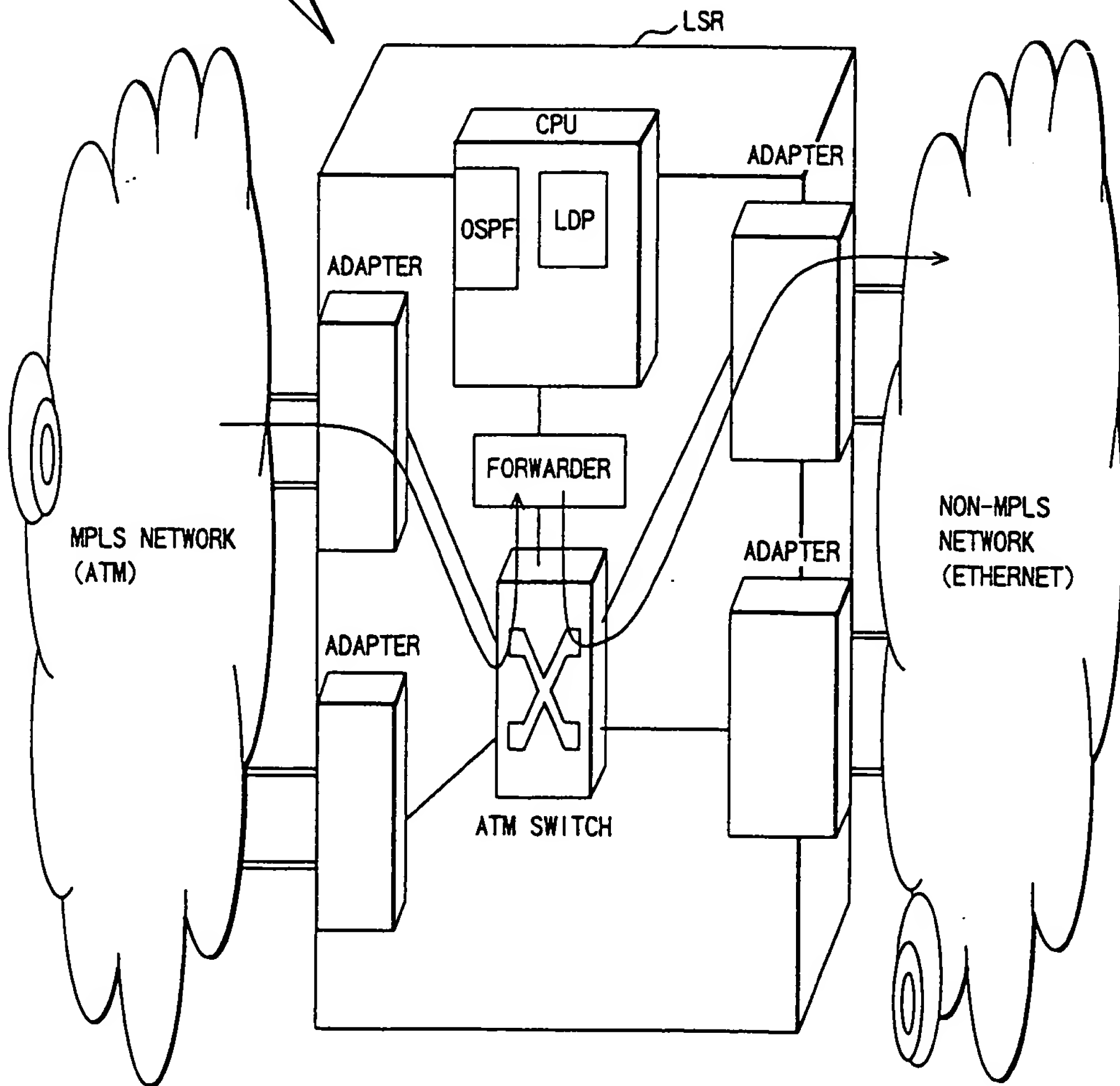


FIG.3
Prior Art

EXAMPLE OF ARCHITECTURE OF LSR AS ATM-SWITCH BASE MPLS
EDGE DEVICE (IP/MPLS FORWARDER IS MOUNTED IN EACH ADAPTER)

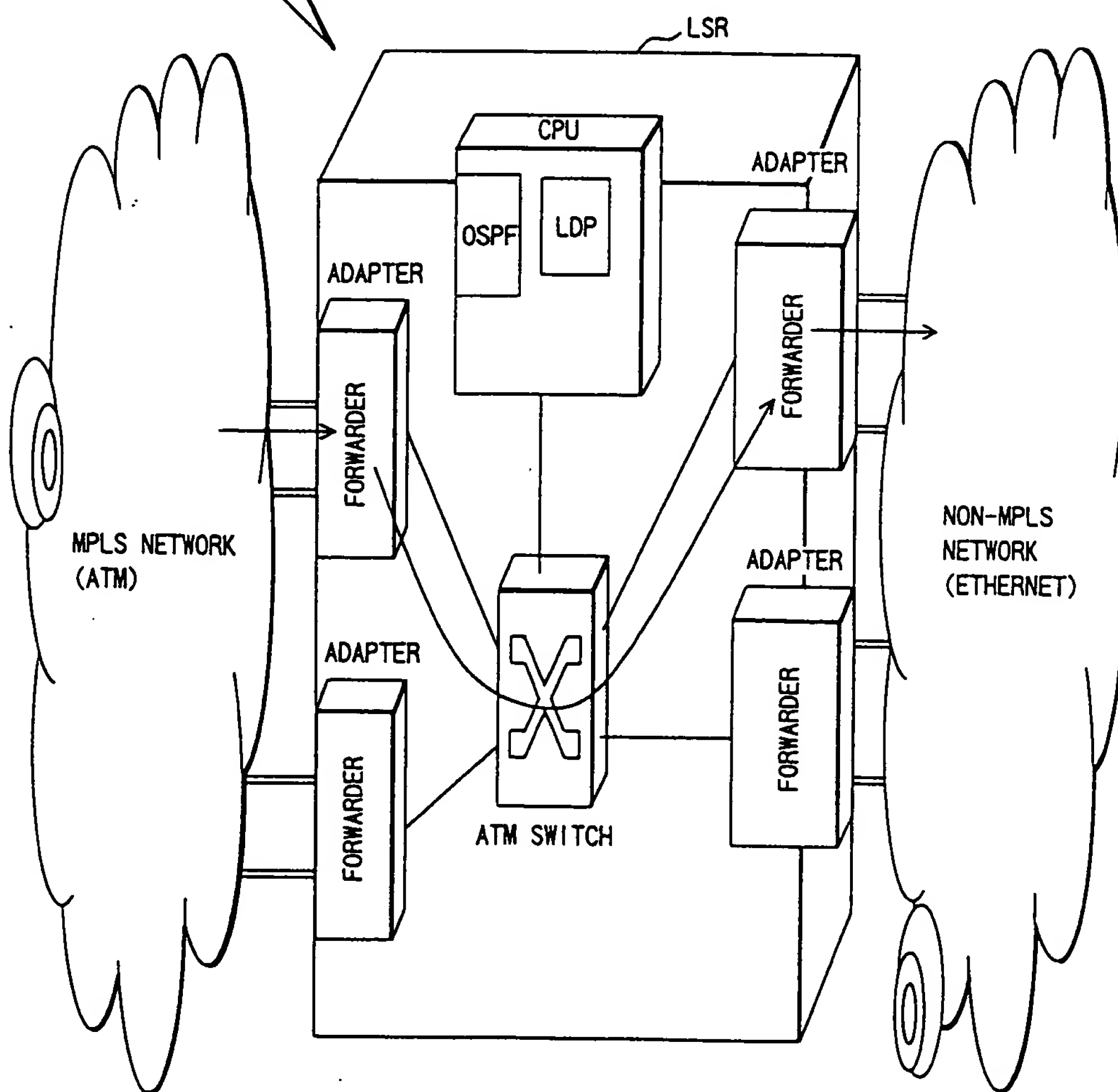
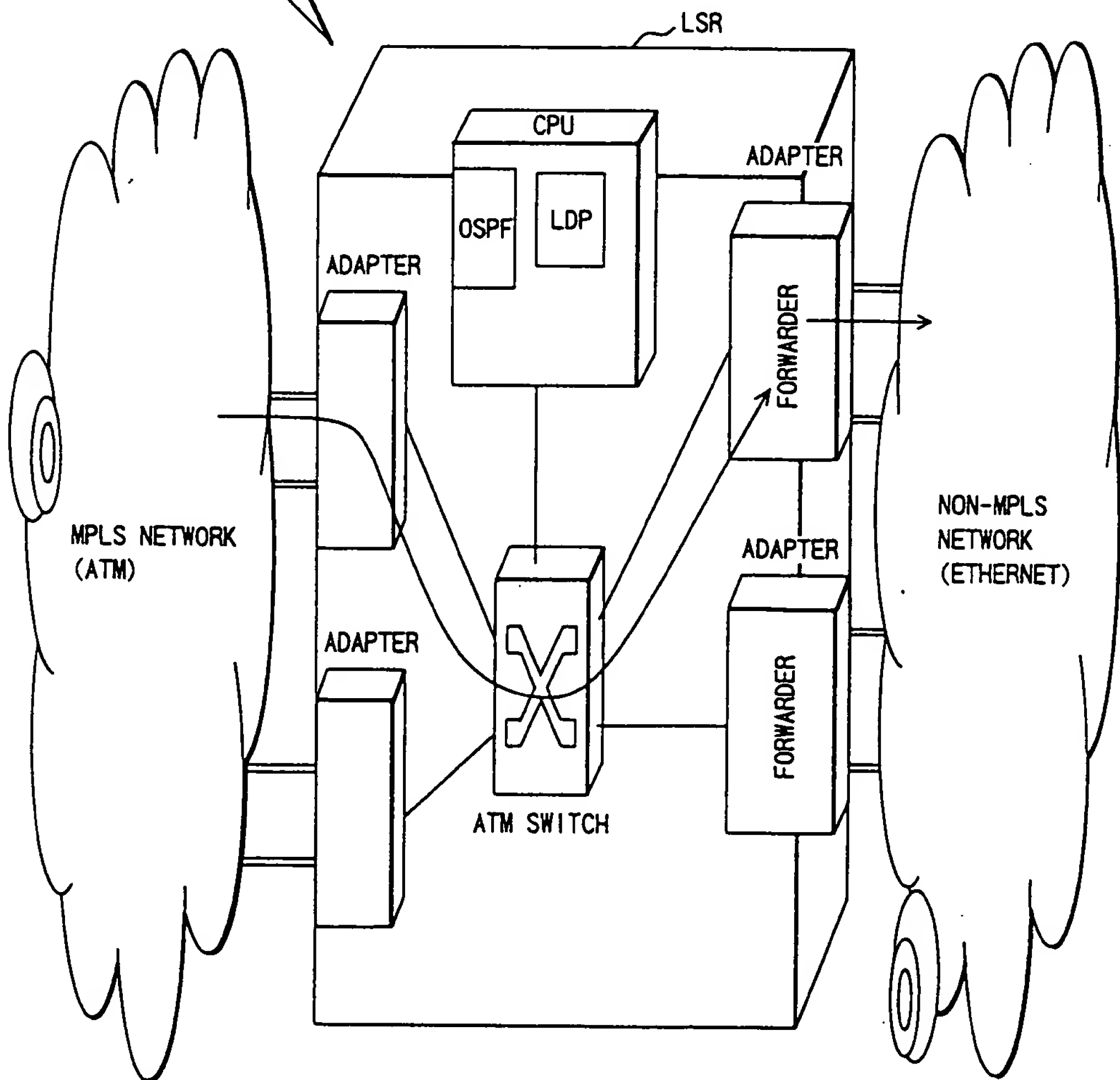


FIG.4
Prior Art

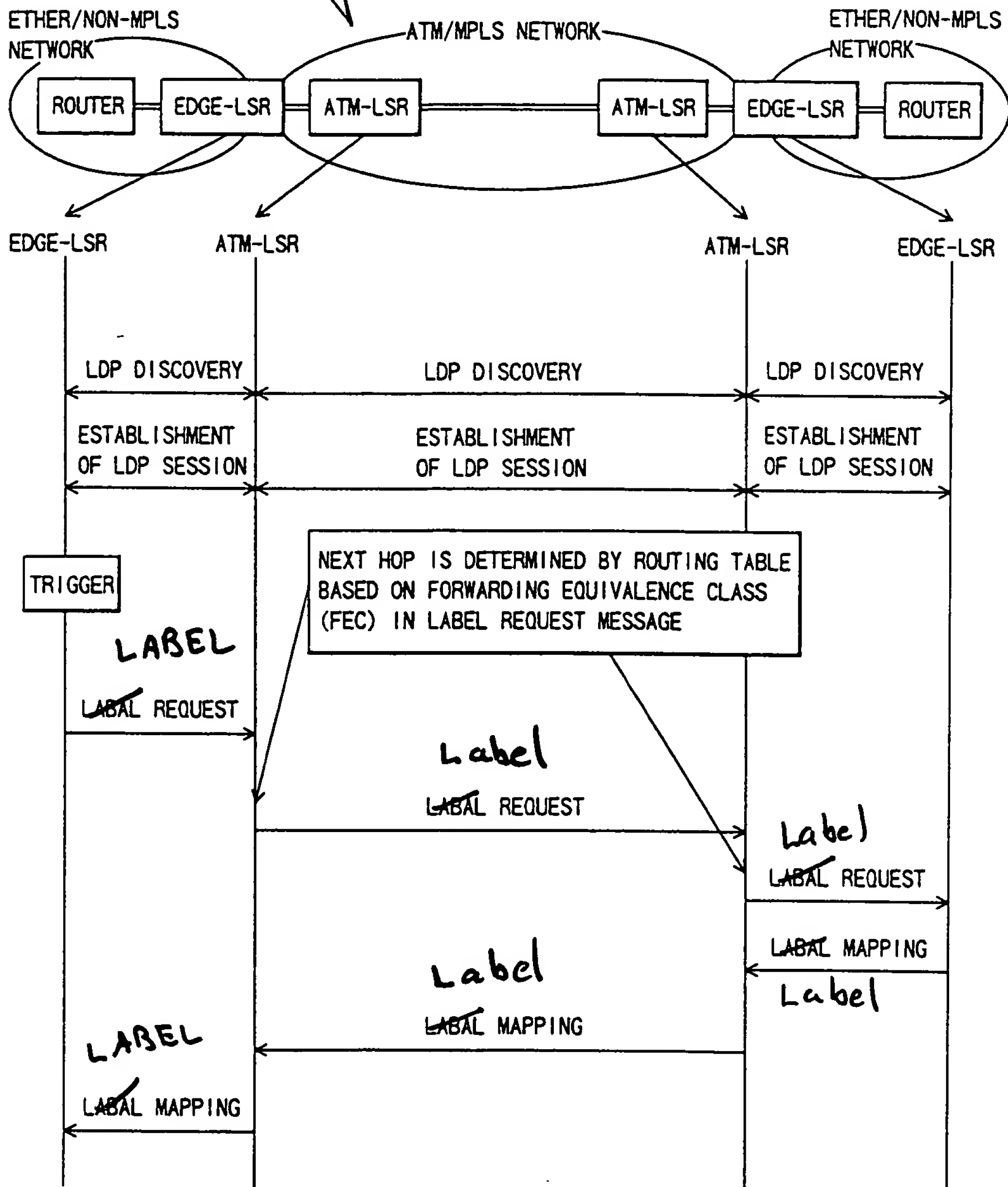
EXAMPLE OF ARCHITECTURE OF LSR AS ATM-SWITCH BASE MPLS
EDGE DEVICE (IP/MPLS FORWARDER IS MOUNTED IN ADAPTER AT
EGRESS FOR PACKETS)



Label

FIG.5
Prior Art

Label DISTRIBUTION SEQUENCE BY HOP BY HOP ROUTING IN MPLS (LDP-BASED CASE: EXAMPLE OF DOWNSTREAM-ON-DEMAND/ ORDERED CONTROL)



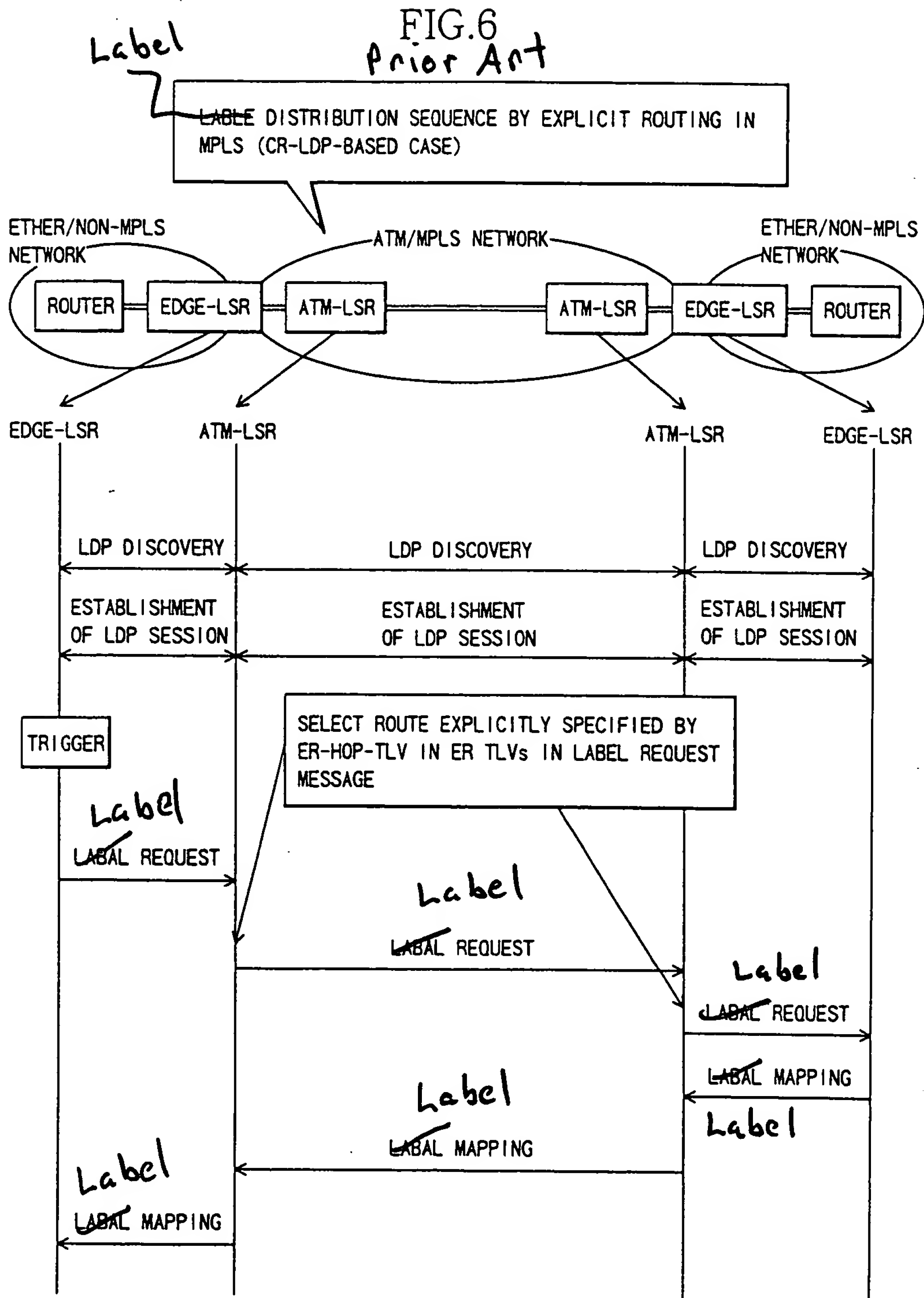


FIG.7

Label

Prior Art

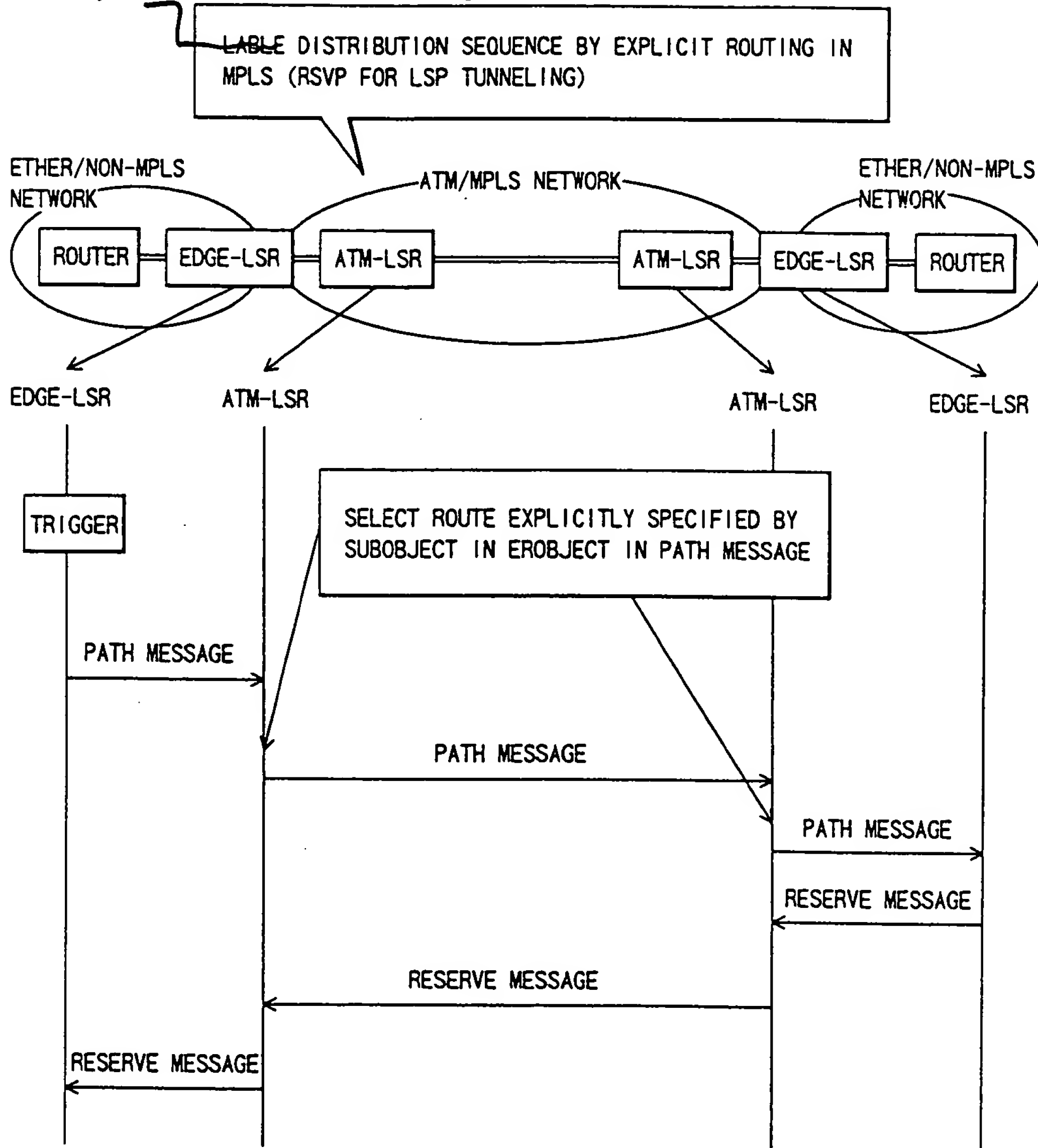


FIG.11

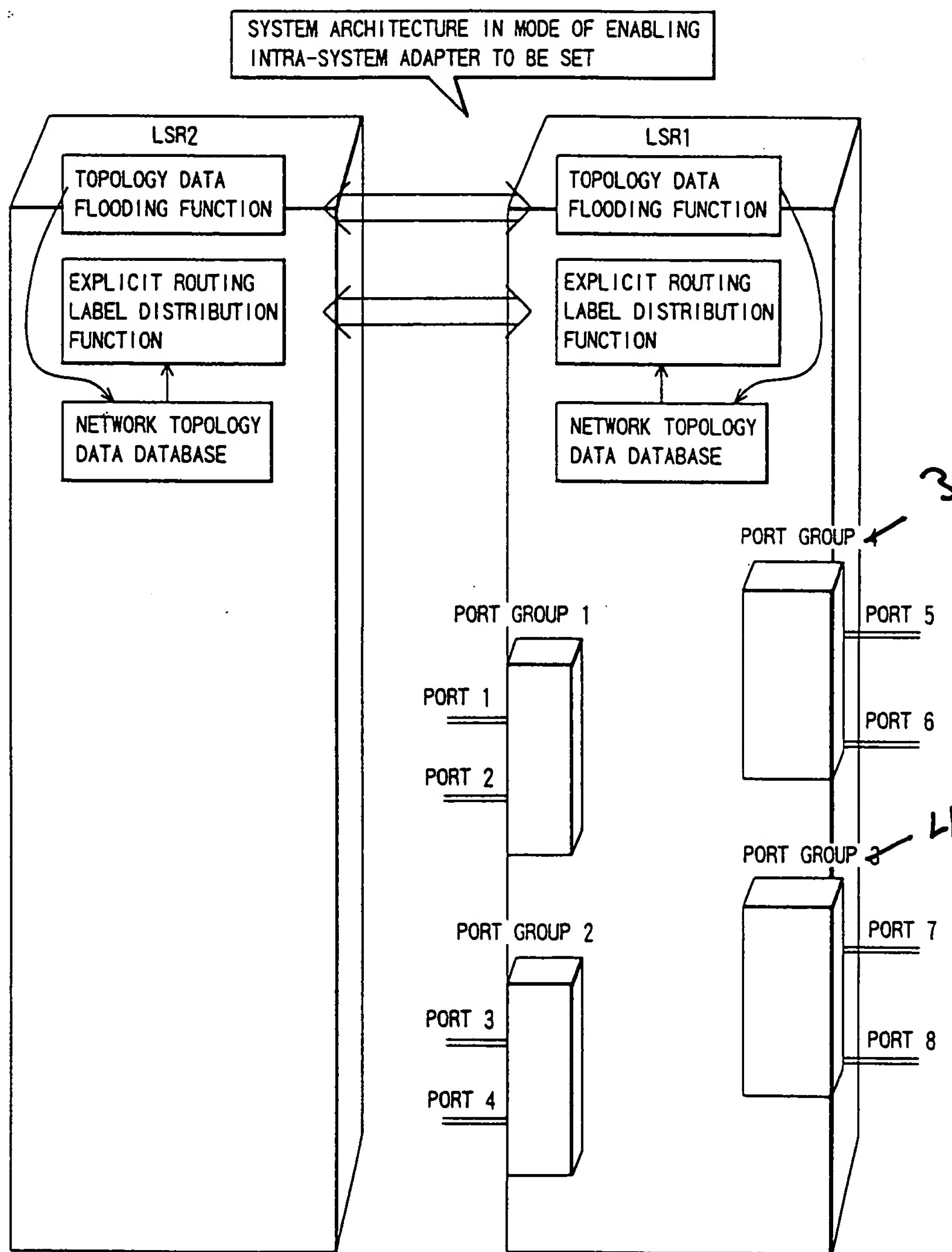


FIG.25

